

Docket No.: P2000,0186



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By: _____

Date: September 18, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Olaf Storbeck
Applic. No. : 09/941,824
Filed : August 29, 2001
Title : Susceptor for Semiconductor Wafers

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U. S. Patent 5,872,694 (Hoinkis et al.) dated February 16, 1999;

U. S. Patent 5,893,760 (Mikata et al.) dated April 13, 1999;

U. S. Patent 6,025,099 (Slonaker) dated February 15, 2000;

U. S. Patent 5,958,198 (Banholzer et al.) dated September 28, 1999;

U. S. Patent 5,580,388 (Moore) dated December 3, 1996;

Patent Abstracts of Japan 61206225 A (Sano Hidezo) dated September 12, 1986;

Patent Abstracts of Japan 02151024 A (Kii Takeshi et al.) dated June 11, 1990;

Patent Abstracts of Japan 04149555 A (Yoshitake Hiroshi) dated May 22, 1992.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the Applicants.

Respectfully submitted,

For Applicants WERNER H STEMER
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Date: September 18, 2001

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